

# 2  
11/5/01  
MWS

XA-9546

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Tadaki SAKURABA et al.

Appln. No.:

Filed: Herewith

For: SEMICONDUCTOR DEVICE AND MANUFACTURE METHOD OF THAT

11017 U.S. PTO  
09/939669  
08/28/01

\* \* \*

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO-1449 are hereby cited.

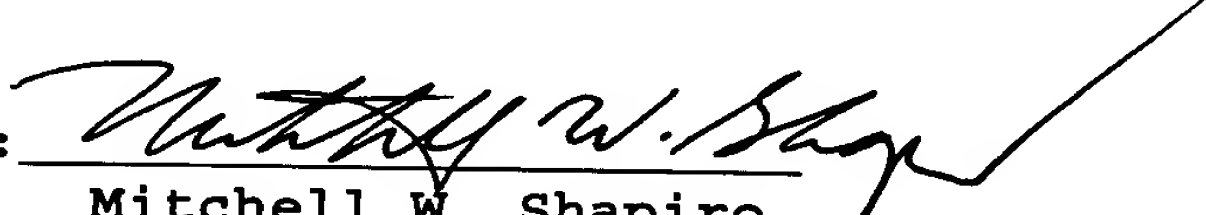
The documents on the attached List are cited in the specification, on pages 2, 3 and 8-10, and their relevance is indicated therein.

Respectfully submitted,

MWS:sjk

Miles & Stockbridge P.C.  
1751 Pinnacle Drive  
Suite 500  
McLean, Virginia 22102-3833  
(703) 903-9000

By:

  
Mitchell W. Shapiro  
Reg. No. 31,568

August 28, 2001

FORM PTO-1449				Atty. Docket No. XA-9546		Appln. No.	
<b>LIST OF DOCUMENTS CITED BY APPLICANT</b>							
				Applicant Tadaki SAKURABA et al.			
				Filing Date HEREWITH		Group	
<b>U.S. PATENT DOCUMENTS</b>							
Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date
	AA						
	AB						
	AC						
	AD						
	AE						
<b>FOREIGN PATENT DOCUMENTS</b>							
Examiner Initial		Document Number	Date	Country	Class	Sub-class	Translation
	AL	10-270603	10/09/98	Japan			Abstract
	AM	10-34699	02/10/98	Japan			Abstract
	AN	10-70217	03/10/98	Japan			Abstract
	AO						
	AP						
	AQ						
<b>OTHER</b> (including author, title, date, pertinent pages, etc.)							
	AR						
	AS						
	AT						
Examiner				Date Considered			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							